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## <u>IN THE UNITED STATES PATENT AND TRADEMARK OFFICE</u>

Applicant: Daniel G. Borkowski et al.

Title: CYCLE-EFFICIENT TDM TIMESLOT REMAPPING FOR NETWORK PROCESSING

**ENGINES** 

Docket No.: Intel-007PUS Serial No.: 10/723,804

Filed: November 26, 2003 Due Date: December 11, 2007

Examiner: Kibrom T. Hailu Group Art Unit: 2616
Confirmation No.: 7999

Commissioner for Patents

Notice of Allowance Date:

Attn: MAIL STOP ISSUE FEE September 11, 2007

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